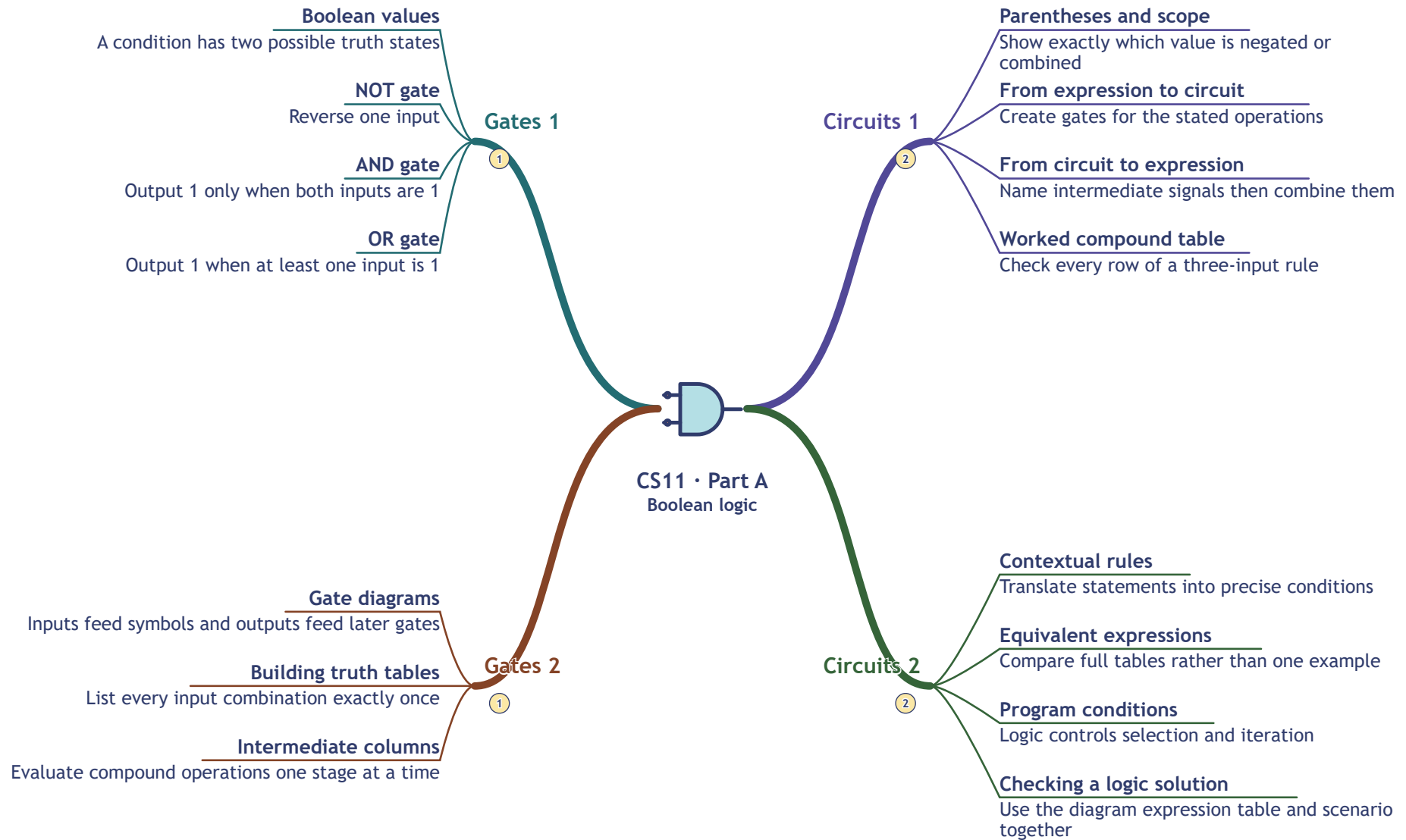




1. Gates 1 → Gates 2: Gate definitions determine every row and intermediate truth value.
2. Circuits 1 → Circuits 2: A circuit and expression must agree for all input combinations.